Bahria University,

Karachi Campus

A picture containing text, room

Description automatically generated

LAB EXPERIMENT NO:

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10

LIST OF TASKS

|  |  |
| --- | --- |
| TASK NO | OBJECTIVE |
|  |  |
| 01 | Simplify the following equations using Boolean Laws and design the circuits of both given as well as simplified equations on multisim. Construct the Truth Tables to verify that the simplified equations give the same result as that of the original equation.   1. F = AB + A (B + C) + B (B + C) 2. F = (A + C) (AD + AD′) + AC + C 3. F = A′ (A + B) + (B + A) (A + B′) 4. F = A ⋅ (B + C) + A′ ⋅ (B + C) 5. F = A′ B′ C′ D + A′ B′ CD + A′ BCD + ABCD + AB′ CD |
|  |  |

Submitted On:

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/12/23

(Date: DD/MM/YY)